



L.W. Golick 2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): L.W. Golick  
Case: 2  
Serial No.: 10/672,495  
Filing Date: September 26, 2003  
Group: To Be Assigned  
Examiner: To Be Assigned

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature: *V. Brucilanni* Date: December 2, 2003

Title: Packaged Integrated Circuit Providing  
Trace Access to High-Speed Leads

TRANSMITTAL OF FORMAL DRAWINGS

Mail Stop PGPUB Drawings  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Attention: Official Draftsperson

Sir:

Applicant submits herewith three (3) sheets of formal drawings in the above-referenced application.

Respectfully submitted,

Date: December 2, 2003

Joseph B. Ryan  
Reg. No. 37,922  
Ryan, Mason & Lewis, LLP  
Attorney for Applicant(s)  
90 Forest Avenue  
Locust Valley, NY 11560  
(516) 759-7517